

2/20/04

Sheet 1 of 1

U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No. 10/783,888	
				M-15311 US		Unassigned	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant(s)			
(Use several sheets if necessary)				Om P. Agrawal; Howard Tang; Jack Wong			
				Filing Date		Group	
				Herewith		Unassigned	
U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
lu	AA	5,548,228	08/20/1996	Madurawe			
ue	AB	5,696,455	12/09/1997	Madurawe			
	AC						
	AD						
	AE						
	AF						
Foreign Patent Documents							
							Translation
		Document	Date	Country	Class	Subclass	Yes No
	AG						
	AH						
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
le	AI	Actel Corporation, "ProASIC ^{PLUS} Flash Family FPGAs", v3.4 December 2003, pages i through iv and 1-1 through 1-64 & 2-1 through 2-67 & 2-1 through 2-7 (list of changes).					
l	AJ	Lattice Semiconductor Corporation, "ispXPGA TM Family", Preliminary Data Sheet, September 2003, pages 1-112.					
le	AK	U.S. Patent Application Serial No. 10/439,602, filed on 05/16/2003, entitled "Non-Volatile And Reconfigurable Programmable Logic Devices"					
Examiner		Date Considered					
bow v		8/29/05					
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.							